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second register set as specified by said instruction, and writes a result value to said first register set or said second register set as specified by said instruction.

Remarks

Upon entry of the foregoing, claims 31-43 are pending in the application. Claim 31 is sought to be amended. This change is believed not to introduce new matter and its entry is respectfully requested.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Robert Sokohl
Attorney for Applicants

Registration No. 36,013

Date: May 20, 1999

1100 New York Avenue, N.W. Suite 600 Washington, D.C. 20005-3934 (202) 371-2600

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